

Circuitry for widening the effective range of a transmitter

The present invention concerns a circuitry by which at least two power control ranges can be established in a transmitter consisting of an amplifier of one or several stages.

In a transmitter of a radio telephone, class-C amplifiers are generally used, which are characterized by good efficiency, 60 to 80 per cent, on high power levels. Because of the great non-linearity of the amplifier, difficulties arise in realizing controlled power control on low power levels, and in addition, when low power levels are used, the efficiency of the amplifier reduces. This causes no harm when a system (or locality of use) is in question in which very low transmission power levels need not be used. The situation will be different, for instance in the digital GSM system covering in future all Europe, in which very low power levels will be in use. If a telephone is going to be used in an aeroplane, it is necessary to use, in order to prevent potential interference to be caused in the aviation electronics, extremely low power levels.

A typical principle block diagram of a transmitter for a GSM radio telephone is presented in Fig. 1. The block diagram only shows the blocks required for understanding the operation. A signal to be transmitted enters e.g. the input RFin of a three-stage class-C amplifier 1. The gain of the amplifier 1 is controlled by a reference amplifier 3, the output of which is filtered before being input in the power amplifier 1. The input signals of the reference amplifier 3 are the voltage derived from a power detector 2, said voltage being proportional to the output voltage RFout of the power amplifier 1, and the control voltage TXC1 derived from the logic section of the telephone. Said blocks 1, 2 and 3 constitute a control loop which tends to be controlled in a state in which the voltage to be derived from the power detector 2 and the control voltage TXC1 derived from the logic sections of the radio telephone are of equal magnitude.

With the procedure of prior art, such power control range can be obtained which extends e.g. from + 13 dBm to + 33 dBm. If a wider power control range is wished, the following difficulties arise: the narrowness and non-linearity of the dynamics range and non-linearity of the power detector and the poor controllability of the power amplifier operating in class-C make the widening of the power range difficult.

The object of the present invention is to provide a procedure by which, when a power amplifier switch known in the art is used, the power control range can be significantly widened.

This is so achieved that a controllable switch has been connected with a power amplifier in se-

ries thereafter, said switch, while in a first state, does not substantially attenuate the power (RF power) of the radio frequency emitted from the power amplifier derived in the output of the transmitter, whereby a first power control range of the transmitter is formed, but while in a second state, it controls part of the RF power emitted from the power amplifier to be consumed in a resistive member and permits part of the power to be conducted to the output of the transmitter, whereby a second power control range is formed.

According to the invention, when the transmitter is in operation, part of the RF power emitted from the power amplifier may be directed aside and be consumed in a resistive element, e.g. in a resistor, and the rest thereof being directed to the output of the transmitter to be conducted further to the antenna. If the control range of the power amplifier is marked P...Pmax, this is the control range in the output of the transmitter which is obtained when the power is not conducted aside, if part of the power is conducted aside with a switch, for the new control range in the output of the transmitter is $(P...Pmax) - p_e$, in which p_e is the power conducted aside. In this manner two power control ranges formed, which may partly overlap or be delimited to one another.

The invention is described below more in detail, referring to the annexed figures, in which

Fig. 1 presents a principal block diagram of a transmitter typical of a GSM radio telephone, and

Fig. 2 presents a principle block diagram of the circuitry of the invention.

The operation of the transmitter of the prior art GSM radio telephone is already described above. With said circuitry it is possible to achieve, as was mentioned above, for the power control range in the output Rfout of the transmitter the range $P...Pmax$. Let us assume that this power control range were in practice +13 dBm to +33 dBm. In the circuitry of the Invention presented in Fig. 2, a PIN diode switch 4 is in connection of the invention after the power amplifier 1 and connected in series therewith, said switch being controlled with a control voltage TXA derived from the control logic of the radio telephone. For the PIN diode switch 4, designs known in the art may be used, such as a switch constructed e.g. from two PIN diodes and from a quarter-wave line. To the gate 3 of the switch is carried control voltage TXA and to the same gate a ground resistance R1 is connected across a separation capacitor C. When it is desired that the transmitter acts in a power range of e.g. +13 dBm to +33 dBm, the control voltage TXA derived from the logic is in zero state and no

current flows across the diodes of the switch 4. Hereby, the RF power generated by the power amplifier transfers along the transmission line of the switch to the transmitter output RFout. By controlling the power amplifier with control voltage TXCI derived from the logic the output power may be controlled in said range.

If one wants to use a power control range of a lower level, the control logic of the radio telephone directs the signal TXA to state logical one (e.g. +5V). Now current flows across the diodes of the PIN diode switch, and the state of the switch changes. The end of the transmission line RFout of the switch towards the connector shorts circuits. Hereby, the transmission line looks open when viewed at the end of the power amplifier 1, and most of the transmitted power is conducted across the PIN diode located at this end to the resistance R1. If the attenuation of the PIN diode switch in this state from gate 1 to gate 2 is e.g. 20 dB, the lower power control range is going to be (+ 13 dB to + 33 dB) - 20 dB, or from - 7 dB to + 13 dB. In this power control range the power is controlled as in the first range, by controlling the power amplifier 1.

The circuitry of the invention has the benefit that it makes the widening of the power range feasible in a simple and inexpensive way. The power detector of the transmitter is not required to have a wide dynamics range, it is enough that it covers the range P to Pmax. Only one analogous control voltage TXCI is needed in the circuitry. The power control range may be widened further by using a several-stage PIN diode attenuator subsequent to the switch. If the power at said point permits its use, in practice one switch is enough with subscriber telephone power, after which a controllable amplifier may be positioned. Within the protective scope of the claims, the practical implementation of the circuit may greatly differ from what is presented above. The power amplifier may be of a different type from the one described above, and the PIN diode switch may be realized in a number of different ways.

Claims

1. A circuitry for widening the power control range of a transmitter of a radio telephone when amplifying a radio frequency signal in a controllable power amplifier of one or several stages, characterized in that together with the power amplifier (1) is connected thereafter in series a controllable switch (4), which, while at a first state, does not substantially attenuate the RF power emitted from the power amplifier (1) derived in the output (RFout) of the transmitter, whereby a first power control range of the transmitter is formed, but which, while in a

second state, directs part of the RF power emitted from the power amplifier (1) to be consumed in a resistive element (R1) and allows part of the power to be transmitted to the output (RFout) of the transmitter, whereby a second power control range is formed.

2. Circuitry according to claim 1, characterized in that the switch (4) to be controlled is a PIN diode switch.

10 3. Circuitry according to claim 2, characterized in that the switch (4) consists of two PIN diodes and of a quarter-wave transmission line.

4. Circuitry according to claim 1, characterized in that the state of the switch (4) is controlled by the logic circuit of the radio telephone.

15 5. Circuitry according to claim 1, characterized in that after the controllable switch (4) is connected a PIN diode attenuator of several stages, or a controllable amplifier, whereby a number of power control ranges of the transmitter may be produced.

6. Circuitry according to claim 1, characterized in that the circuitry is used in the transmitter of a digital radio telephone.

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